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Joe Greco. **DPN : Design Product News.** Willowdale: Jan/Feb 2002. Vol. 30, Iss. 1; p. 25 (1 page)

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Robert Mills. **Computer - Aided Engineering.** Cleveland: Sep 1998. Vol. 17, Iss. 9; p. 56 (6 pages)

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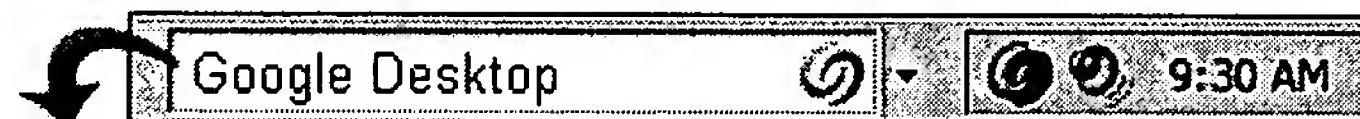File Format: PDF/Adobe Acrobat - View as HTML
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1 A Graph Theoretic Approach for Design and Synthesis of Multiplierless FIR Filters   ☐
Khurram Muhammad, Kaushik Roy
November 1999 **Proceedings of the 12th international symposium on System synthesis**
Publisher: IEEE Computer Society
Full text available: 🔲 pdf(265.90 KB)   Additional Information: full citation, abstract
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We present a novel approach which can be used to obtain multiplierless implementations of finite impulse response (FIR) digital filters. The main idea is to reorder filter coefficients such that an implementation based on differential coefficients requires only a few adders. We represent this problem using a graph in which vertices represent the coefficients and edges represent the resources required when the differential coefficient corresponding to the edge is used in a computation. We also pre ...

2 Approximation & refinement: Dynamic remeshing and applications   ☐
J. Vorsatz, Ch. Rössl, H.-P. Seidel
June 2003 **Proceedings of the eighth ACM symposium on Solid modeling and applications**
Publisher: ACM Press
Full text available: 🔲 pdf(1.53 MB)   Additional Information: full citation, abstract, references, index terms

Triangle meshes are a flexible and generally accepted boundary representation for complex geometric shapes. In addition to their geometric qualities or topological simplicity, intrinsic qualities such as the shape of the triangles, their distribution on the surface and the connectivity are essential for many algorithms working on them. In this paper we present a flexible and efficient remeshing framework that improves these *intrinsic* properties while keeping the mesh geometrically close t ...

Keywords: dynamic meshes, multiresolution modeling, remeshing

3 Application performance improvement on the iPSC/2 computer   ☐
S. Arshi, R. Asbury, J. Brandenburg, D. Scott
January 1988 **Proceedings of the third conference on Hypercube concurrent computers and applications: Architecture, software, computer systems, and general issues - Volume 1**
Publisher: ACM Press

Full text available: pdf(299.73 KB)   Additional Information: full citation, abstract, references, index terms

The performance of concurrent computers depends fundamentally on the capabilities of the individual processing nodes and the characteristics of the interprocessor communication system. The Intel iPSC®/2 is significantly better in both categories than the original iPSC (iPSC/1). This paper will briefly compare the hardware of the two machines and then discuss the actual measured performance improvement of several kernels of application codes. The improvements vary from a factor of two an ...

4 Linear-time encodable and decodable error-correcting codes

Daniel A. Spielman
May 1995 **Proceedings of the twenty-seventh annual ACM symposium on Theory of computing**
Publisher: ACM Press

Full text available: pdf(993.36 KB)   Additional Information: full citation, references, citings, index terms

5 Hierarchical microprogram generating system

Eiji Tamura, Mario Tokoro
November 1979 **ACM SIGMICRO Newsletter , Proceedings of the 12th annual workshop on Microprogramming MICRO 12**, Volume 10 Issue 4
Publisher: IEEE Press, ACM Press

Full text available: pdf(1.04 MB)   Additional Information: full citation, abstract, references, citings, index terms

A general purpose microprogram generating system has been developed to compose, together with a hardware/microprogram debugger, a general purpose development support system for LSI Processor Modules such as Am2900, MMI6700, and/or MACROLOGIC. The microprogram generator, designed to be applicable to a wide variety of microinstruction sets from vertical to horizontal including sophisticated control schemes like pipelining, has a three-level hierarchical structure. The lowest level generator i ...

6 Systematic hardware adaptation of systolic algorithms

M. Valero-Garcia, J. J. Navarro, J. M. Llaberia, M. Valero
April 1989 **ACM SIGARCH Computer Architecture News , Proceedings of the 16th annual international symposium on Computer architecture ISCA '89**, Volume 17 Issue 3
Publisher: ACM Press

Full text available: pdf(1.00 MB)   Additional Information: full citation, abstract, references, index terms

In this paper we propose a methodology to adapt Systolic Algorithms to the hardware selected for their implementation. Systolic Algorithms obtained can be efficiently implemented using Pipelined Functional Units. The methodology is based on two transformation rules. These rules are applied to an initial Systolic Algorithm, possibly obtained through one of the design methodologies proposed by other authors. Parameters for these transformations are obtained from the specification of the hardw ...

7 Random variate generation for multivariate unimodal densities

Luc Devroye
October 1997 **ACM Transactions on Modeling and Computer Simulation (TOMACS)**, Volume 7 Issue 4
Publisher: ACM Press

Full text available: pdf(303.84 KB)   Additional Information: full citation, abstract, references, citings, index terms, review

A probability density on a finite-dimensional Euclidean space is orthounimodal with a given mode if within each orthant (quadrant) defined by the mode, the density is a monotone

function of each of its arguments individually. Up to a linear transformation, most of the commonly used random vectors possess orthounimodal densities. To generate a random vector from a given orthounimodal density, several general-purpose algorithms are presented; and an experimental performance evaluation illustr ...

Keywords: multivariate densities, nonparametric classes, random variate generation, unimodality

8 iPSC/2 system: a second generation hypercube

R. Arlauskas

January 1988 **Proceedings of the third conference on Hypercube concurrent computers and applications: Architecture, software, computer systems, and general issues - Volume 1**

Publisher: ACM Press

Full text available: pdf(458.46 KB)   Additional Information: full citation, references, citings, index terms

9 Invited talks: Hardware acceleration of graphics and imaging algorithms using FPGAs

Pavel Zemcik

April 2002 **Proceedings of the 18th spring conference on Computer graphics**

Publisher: ACM Press

Full text available: pdf(842.76 KB)   Additional Information: full citation, abstract, references, citings, index terms

Computer graphics algorithms and algorithms used in image processing are generally computationally expensive. This fact is the reason why people struggle to accelerate such algorithms using any reasonable means. The traditional sources of speedup are faster processors, parallelism, or dedicated hardware. Development in digital circuit technology, especially rapid development of Field Programmable Gate Arrays (FPGA), offers alternative way to acceleration. Current FPGA chips are capable of runnin ...

Keywords: FPGA, computer graphics, hardware acceleration, image processing

10 Time, clocks, and the ordering of events in a distributed system

Leslie Lamport

July 1978 **Communications of the ACM**, Volume 21 Issue 7

Publisher: ACM Press

Full text available: pdf(854.56 KB)   Additional Information: full citation, abstract, references, citings, index terms

The concept of one event happening before another in a distributed system is examined, and is shown to define a partial ordering of the events. A distributed algorithm is given for synchronizing a system of logical clocks which can be used to totally order the events. The use of the total ordering is illustrated with a method for solving synchronization problems. The algorithm is then specialized for synchronizing physical clocks, and a bound is derived on how far out of synchrony the clock ...

Keywords: clock synchronization, computer networks, distributed systems, multiprocess systems

11

Robbers, marshals, and guards: game theoretic and logical characterizations of hypertree width

Georg Gottlob, Nicola Leone, Francesco Scarcello
May 2001 **Proceedings of the twentieth ACM SIGMOD-SIGACT-SIGART symposium on Principles of database systems**
Publisher: ACM Press

Full text available: pdf(293.92 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

In a previous paper [10], the authors introduced the notion of hypertree decomposition and the corresponding concept of hypertree width and showed that the conjunctive queries whose hypergraphs have bounded hypertree-width can be evaluated in polynomial time. Bounded hypertree-width generalizes the notions of acyclicity and bounded treewidth and corresponds to larger classes of tractable queries. In the present paper, we provide natural characterizations of hypergraphs and queries having boun ...

12 Software architecture for a constraint-based virtual environment

Terrence Fernando, Norman Murray, Kevin Tan, Prasad Wimalaratne
December 1999 **Proceedings of the ACM symposium on Virtual reality software and technology**
Publisher: ACM Press

Full text available: pdf(887.05 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

Virtual environment technology is now beginning to be recognised as a powerful design tool in industrial sectors such as Manufacturing, Process Engineering, Construction, Automotive and Aerospace industries. It offers the ability to visualise a design from different viewpoints by engineers from different design perspectives providing a powerful design analysis tool for supporting concurrent engineering philosophy. A common weakness of the current commercial virtual environments is the lack ...

Keywords: component assembly, constraints, tasks, virtual environments

13 Modeling surfaces of arbitrary topology using manifolds

Cindy M. Grimm, John F. Hughes
September 1995 **Proceedings of the 22nd annual conference on Computer graphics and interactive techniques**
Publisher: ACM Press

Full text available: pdf(303.06 KB)
ps(4.32 MB)   Additional Information: <u>full citation</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

14 Power & QoS constrained networks: A routing protocol for power constrained networks with asymmetric links

Guoqiang Wang, Yongchang Ji, Dan C. Marinescu, Damla Turgut
October 2004 **Proceedings of the 1st ACM international workshop on Performance evaluation of wireless ad hoc, sensor, and ubiquitous networks**
Publisher: ACM Press

Full text available: pdf(259.65 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

In many instances, an ad hoc network consists of nodes with different hardware and software capabilities as well as power limitations. This is the case of ad hoc grids where devices such as desktops, laptops, robots, palmtops, sensors, and actuators collaborate to solve computational problems. In such a heterogeneous environment, the nodes have various degrees of mobility and range and the communication links are asymmetric: node i may be able to reach node j, but j may not be able to reach i. A ...

Keywords: forwarding fitness function, m-limited forwarding, m-party proxy set

15 Special issue on learning from imbalanced datasets: Extreme re-balancing for SVMs: ☐
a case study
Bhavani Raskutti, Adam Kowalczyk
June 2004 **ACM SIGKDD Explorations Newsletter**, Volume 6 Issue 1
Publisher: ACM Press
Full text available: 📄 pdf(254.41 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>

> There are many practical applications where learning from single class examples is either,
> the only possible solution, or has a distinct performance advantage. The first case occurs
> when obtaining examples of a second class is difficult, e.g., classifying sites of "interest"
> based on web accesses. The second situation is exemplified by the gene knock-out
> experiments for understanding Aryl Hydrocarbon Receptor signalling pathway that
> provided the data for the second task of the KDD 2002 Cup, wher ...

16 Mental Registration of 2D and 3D Visualizations (An Empirical Study) ☐
Melanie Tory
October 2003 **Proceedings of the 14th IEEE Visualization 2003 (VIS'03) VIS '03**
Publisher: IEEE Computer Society
Full text available: 📄 pdf(333.20 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>

> 2D and 3D views are used together in many visualization domains, such as medical
> imaging, flow visualization, oceanographic visualization, and computer aided design (CAD).
> Combining these views into one display can be done by: (1) orientation icon (i.e., separate
> windows), (2) in-place methods (e.g., clip and cutting planes), and (3) a new method
> called ExoVis. How 2D and 3D views are displayed affects ease of mental registration
> (understanding the spatial relationship between views), an importa ...

Keywords: 2D and 3D visualization, mental registration, slice, orthographic projection,
empirical study, experiment

17 Combining 2D and 3D views for orientation and relative position tasks ☐
Melanie Tory, Torsten Moller, M. Stella Atkins, Arthur E. Kirkpatrick
April 2004 **Proceedings of the SIGCHI conference on Human factors in computing
        systems**
Publisher: ACM Press
Full text available: 📄 pdf(1.57 MB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

> We compare 2D/3D combination displays to displays with 2D and 3D views alone.
> Combination displays we consider are: orientation icon (i.e., side-by-side), in-place
> methods (e.g., clip planes), and a new method called ExoVis. We specifically analyze
> performance differences (i.e., time and accuracy) for 3D orientation and relative position
> tasks. Empirical results show that 3D displays are effective for approximate navigation and
> relative positioning whereas 2D/3D combination displays (orientation ...

Keywords: 2D and 3D visualization, display design, empirical study, experiment,
orientation and relative position tasks

18 Papers: Distributed core multicast (DCM): a multicast routing protocol for many ☐
groups with few receivers
Ljubica Blazević, Jean-Yves Le Boudec
October 1999 **ACM SIGCOMM Computer Communication Review**, Volume 29 Issue 5
Publisher: ACM Press

Full text available: pdf(1.48 MB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>

We present a multicast routing protocol called Distributed Core Multicast (DCM). It is intended for use within a large single Internet domain network with a very large number of multicast groups with a small number of receivers. Such a case occurs, for example, when multicast addresses are allocated to mobile hosts, as a mechanism to manage Internet host mobility or in large distributed simulations. For such cases, existing dense or sparse mode multicast routing algorithms do not scale well with ...

19 <u>Collaboration and cooperation -- I: A 2D-3D integrated environment for cooperative work</u>
Kousuke Nakashima, Takashi Machida, Kiyoshi Kiyokawa, Haruo Takemura
November 2005 **Proceedings of the ACM symposium on Virtual reality software and technology VRST '05**
Publisher: ACM Press
Full text available: pdf(3.00 MB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

This paper proposes a novel tabletop display system for natural communication and flexible information sharing. The proposed system is specifically designed for integration of 2D and 3D user interfaces, using a multi-user stereoscopic display, IllusionHole. The proposed system takes awareness into consideration and provides both 2D and 3D information and user interfaces. On the display, a number of standard Windows desktop environments are provided as personal workspaces, as well as a shared wor ...

Keywords: 2D - 3D integrated user interface, IllusionHole, VNC

20 <u>Spatial Cognition: Evaluating the effectiveness of spatial memory in 2D and 3D physical and virtual environments</u>
Andy Cockburn, Bruce McKenzie
April 2002 **Proceedings of the SIGCHI conference on Human factors in computing systems: Changing our world, changing ourselves**
Publisher: ACM Press
Full text available: pdf(1.15 MB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

User interfaces can improve task performance by exploiting the powerful human capabilities for spatial cognition. This opportunity has been demonstrated by many prior experiments. It is tempting to believe that providing greater spatial flexibility-by moving from flat 2D to 3D user interfaces-will further enhance user performance. This paper describes an experiment that investigates the effectiveness of spatial memory in real-world physical models and in equivalent computer-based virtual systems ...

Keywords: 3D user interfaces, document management, information visualization, spatial memory

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1 Special issue: AI in engineering   ▬
    D. Sriram, R. Joobbani
    April 1985 **ACM SIGART Bulletin**, Issue 92
    Publisher: ACM Press
    Full text available: 🗎 pdf(8.79 MB)   Additional Information: full citation, abstract

    The papers in this special issue were compiled from responses to the announcement in the
    July 1984 issue of the SIGART newsletter and notices posted over the ARPAnet. The
    interest being shown in this area is reflected in the sixty papers received from over six
    countries. About half the papers were received over the computer network.

2 A roadmap of CAD tool changes for sub-micron interconnect problems   ▬
    Lou Scheffer
    April 1997 **Proceedings of the 1997 international symposium on Physical design**
    Publisher: ACM Press
    Full text available: 🗎 pdf(859.77 KB)   Additional Information: full citation, references, citings, index terms

3 Architecture analysis and automation: Post-placement C-slow retiming for the xilinx   ▬
    virtex FPGA
    Nicholas Weaver, Yury Markovskiy, Yatish Patel, John Wawrzynek
    February 2003 **Proceedings of the 2003 ACM/SIGDA eleventh international
                    symposium on Field programmable gate arrays**
    Publisher: ACM Press
    Full text available: 🗎 pdf(222.65 KB)   Additional Information: full citation, abstract, references, citings, index terms

    C-slow retiming is a process of automatically increasing the throughput of a design by
    enabling fine grained pipelining of problems with feedback loops. This transformation is
    especially appropriate when applied to FPGA designs because of the large number of
    available registers. To demonstrate and evaluate the benefits of C-slow retiming, we
    constructed an automatic tool which modifies designs targeting the Xilinx Virtex family of
    FPGAs. Applying our tool to three benchmarks: AES encryption, Smi ...

    Keywords: C-slow retiming, FPGA CAD, FPGA optimization, retiming

4 Designers and their machines: CAD use and support in the US and Japan

Jeffrey K. Liker, Mitchell Fleischer, Mitsuo Nagamachi, Michael S. Zonnevylle

February 1992 **Communications of the ACM**, Volume 35 Issue 2

Publisher: ACM Press

Full text available: 📄 pdf(15.85 MB)   Additional Information: full citation, references, index terms

Keywords: cross-national research on computer use, management of computing, user assessments, user support

5 Power minimization in IC design: principles and applications

Massoud Pedram

January 1996 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 1 Issue 1

Publisher: ACM Press

Full text available: 📄 pdf(550.02 KB)   Additional Information: full citation, abstract, references, citings, index terms

Low power has emerged as a principal theme in today's electronics industry. The need for low power has caused a major paradigm shift in which power dissipation is as important as performance and area. This article presents an in-depth survey of CAD methodologies and techniques for designing low power digital CMOS circuits and systems and describes the many issues facing designers at architectural, logical, and physical levels of design abstraction. It reviews some of the techniques and tool ...

Keywords: CMOS circuits, adiabatic circuits, computer-aided design of VLSI, dynamic power dissipation, energy-delay product, gated clocks, layout, low power layout, low power synthesis, lower-power design, power analysis and estimation, power management, power minimization and management, probabilistic analysis, silicon-on-insulator technology, statistical sampling, switched capacitance, switching activity, symbolic simulation, synthesis, system design

6 High level CAD melds microsystems with foundries

J. M. Karam, B. Courtois, M. Bauge

March 1996 **Proceedings of the 1996 European conference on Design and Test**

Publisher: IEEE Computer Society

Full text available: 📄 pdf(626.86 KB)
📄 Publisher Site
   Additional Information: full citation, abstract, citings

Computer-aided-design (CAD) software is needed to design microsystems without excessive complexity and design time. Currently available CAD tools, such as Cadence DF2 or Mentor Falcon Framework, need modifications before they can be used for the automated design of micromachined devices. Work at the CMP has extended the capabilities of Cadence OPUS to microsystem technology. The user is able to generate layout of a microsystem including electronic and non-electronic parts in a commonly used form ...

Keywords: CAD, CAD software, CIF format, Cadence OPUS, GDS2 format, Si, automated design, bridges, cantilevers, computer-aided-design, design rules checks, digital simulation, electronic engineering computing, etching, extended extractor, high level CAD, layout generation, membranes, micromachined devices, micromachining, micromechanical devices, microsystem design, netlist generation, parameters extraction, parametrized behavioural models, parametrized cells library, semiconductor process modelling,

simulation

7 Architecture: The microarchitecture of FPGA-based soft processors

Peter Yiannacouras, Jonathan Rose, J. Gregory Steffan

September 2005 **Proceedings of the 2005 international conference on Compilers, architectures and synthesis for embedded systems CASES '05**

Publisher: ACM Press

Full text available: pdf(202.27 KB)   Additional Information: full citation, abstract, references, index terms

As more embedded systems are built using FPGA platforms, there is an increasing need to support processors in FPGAs. One option is the *soft processor*, a programmable instruction processor implemented in the reconfigurable logic of the FPGA. Commercial soft processors have been widely deployed, and hence we are motivated to understand their microarchitecture. We must re-evaluate microarchiteture in the soft processor context because an FPGA platform is significantly different than an ASIC ...

Keywords: ASIP, FPGA, Nios, RTL generation, SPREE, application specic tradeoff, embedded processor, exploration, microarchitecture, pipeline, soft processor

8 User documentation for Design Automation at TI

Diana Mae Sims, James S. Crabbe

June 1981 **Proceedings of the 18th conference on Design automation**

Publisher: IEEE Press

Full text available: pdf(720.83 KB)   Additional Information: full citation, abstract, citings, index terms

The use of documentation is design automation is extensive. The &ldquoDADGUIDE" Documentation System at Texas Instruments' Design Automation Department accomplishes many documentation tasks by means of a set of procedures, local and central computers, word processing programs with macro capabilities, and physical text formatters. The documentation output is resident on a data set at the central computing facility. When users request documents through submission of batch jobs, they exe ...

9 Session IV - hypertext systems: Intermedia: issues, strategies, and tactics in the design of a hypermedia document system

L. Nancy Garrett, Karen E. Smith, Norman Meyrowitz

December 1986 **Proceedings of the 1986 ACM conference on Computer-supported cooperative work**

Publisher: ACM Press

Full text available: pdf(1.20 MB)   Additional Information: full citation, abstract, references, citings

A hypermedia system provides a tool for cooperative work by allowing writers and designers to share a network of linked documents where they can create documents, link their own and others' documents together, and leave notes for one another. This paper discusses issues that designers need to address in the development of hypermedia systems. Major issues involve what kind of linking, contexts, and visual modeling the system provides. The composite of the answers to these issues determines the na ...

10 The impact and use of computer technology by the police

Kent W. Colton

January 1979 **Communications of the ACM**, Volume 22 Issue 1

Publisher: ACM Press

Full text available: pdf(1.29 MB)   Additional Information: full citation, abstract, references, citings

Over the past decade there has been a significant growth in the use of computer technology by U.S. police departments. This growth, however, has been at a slower rate

than predicted in the early 1970's. Further, when computer applications extend beyond "routine" uses to "nonroutine" efforts, such as resource allocation or computer-aided-dispatch systems where the machine begins to become a tool for decision making, strategic planning and person/machine interaction, t ...

Keywords: computer aided dispatch, computer technology, innovation, law enforcement, police command and control, police resource allocation, technology transfer

11 MAELSTROM: efficient simulation-based synthesis for custom analog cells
Michael Krasnicki, Rodney Phelps, Rob A. Rutenbar, L. Richard Carley
June 1999 **Proceedings of the 36th ACM/IEEE conference on Design automation**
Publisher: ACM Press
Full text available: pdf(129.41 KB)   Additional Information: full citation, references, citings, index terms

12 Power grid, thermal, and leakage issues: LAP: a logic activity packing methodology for leakage power-tolerant FPGAs
Hassan Hassan, Mohab Anis, Mohamed Elmasry
August 2005 **Proceedings of the 2005 international symposium on Low power electronics and design ISLPED '05**
Publisher: ACM Press
Full text available: pdf(667.49 KB)   Additional Information: full citation, abstract, references, index terms

As FPGAs enter the nanometer regime, several modifications are needed to reduce the increasing leakage power dissipation. Hence, this work presents some modifications to the FPGAs CAD flow to mitigate leakage power dissipation through the use of multi-threshold CMOS technologies to pack and place logic blocks that exhibit similar idleness close to each other so they can be turned *off* during their idle time. The modifications are integrated into the VPR flow and tested on several FPGA benc ...

Keywords: FPGA, activity profile, basic logic elements (BLEs), configurable logic blocks (CLBs), packing, sleep transistor (ST), sub-threshold leakage power

13 An interactive tool for register-level structure optimization
D. W. Knapp
June 1989 **Proceedings of the 26th ACM/IEEE conference on Design automation**
Publisher: ACM Press
Full text available: pdf(435.65 KB)   Additional Information: full citation, abstract, references, citings, index terms

This paper presents a program named RLEXT (Register Level Exploration Tool), which allows a user to manually modify a register-level datapath design. The allowed operations are addition and deletion of components and connections. Any component not an i/o port, a register, or an addressable memory may be added or deleted. Any wire or connection of a pin to a wire may be added or deleted. The unique aspect of RLEXT is that the continued ability of the datapath to support its behavioral specif ...

14 A prototype framework for knowledge-based analog circuit synthesis
R. Harjani, R. A. Rutenbar, L. R. Carley
October 1987 **Proceedings of the 24th ACM/IEEE conference on Design automation**
Publisher: ACM Press
Full text available: pdf(1.07 MB)   Additional Information: full citation, abstract, references, citings, index terms

An organization for a knowledge-based analog circuit synthesis tool is described. Analog circuit topologies are represented as a hierarchy of functional blocks; a planning mechanism is introduced to translate performance specifications between levels in this circuit hierarchy. A prototype implementation, OASYS, synthesizes sized transistor schematics for simple CMOS operational amplifiers from performance specifications and process parameters, and demonstrates the workability of the approac ...

15 <u>User interface software tools</u>

Brad A. Myers

March 1995 **ACM Transactions on Computer-Human Interaction (TOCHI)**, Volume 2 Issue 1

Publisher: ACM Press

Full text available: <u>pdf(3.25 MB)</u>   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

Almost as long as there have been user interfaces, there have been special software systems and tools to help design and implement the user interface software. Many of these tools have demonstrated significant productivity gains for programmers, and have become important commercial products. Others have proven less successful at supporting the kinds of user interfaces people want to build. This article discusses the different kinds of user interface software tools, and investigates why some ...

Keywords: interface builders, toolkits, user interface development environments, user interface software

16 <u>Future Design Trends: A roadmap and vision for physical design</u>

Andrew B. Kahng

April 2002 **Proceedings of the 2002 international symposium on Physical design**

Publisher: ACM Press

Full text available: <u>pdf(305.03 KB)</u>   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

This invited paper offers "roadmap and vision" for physical design. The main messages are as follows. (1) The high-level roadmap for physical design is static and well-known. (2) Basic problems remain untouched by fundamental research. (3) Academia should not overemphasize back- filling and formulation over innovation and optimization. (4) The physical design field must become more mature and efficient in how it prioritizes research directions and uses its human resources. (5) The scope of physi ...

17 <u>Constraint management for collaborative electronic design</u>

Juan Antonio Carballo, Stephen W. Director

June 1999 **Proceedings of the 36th ACM/IEEE conference on Design automation**

Publisher: ACM Press

Full text available: <u>pdf(1.62 MB)</u>   Additional Information: <u>full citation</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

18 <u>Poster Session: Deformation of finite element meshes using directly manipulated free-form deformation</u>

Norbert Frisch, Thomas Ertl

June 2002 **Proceedings of the seventh ACM symposium on Solid modeling and applications**

Publisher: ACM Press

Full text available: <u>pdf(704.19 KB)</u>   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

CrashViewer [5, 18] is a tool for visualizing car crash simulation input and output data consisting of nite element meshes. For a shorter work ow, a feature for local deformation of the car components represented by FE meshes is desired. This feature allows to quickly make minor corrections and enhancements directly on the FE mesh. The roundtrip through the CAD department and the remeshing of the CAD representation is avoided. The crash simulation can be started immediately with the modified car ...

Keywords: CAD, free-form deformation, nite elements

19 Collision detection and proximity queries

Sunil Hadap, Dave Eberle, Pascal Volino, Ming C. Lin, Stephane Redon, Christer Ericson
August 2004 **Proceedings of the conference on SIGGRAPH 2004 course notes GRAPH '04**
Publisher: ACM Press
Full text available: pdf(11.22 MB)   Additional Information: full citation, abstract

This course will primarily cover widely accepted and proved methodologies in collision detection. In addition more advanced or recent topics such as continuous collision detection, ADFs, and using graphics hardware will be introduced. When appropriate the methods discussed will be tied to familiar applications such as rigid body and cloth simulation, and will be compared. The course is a good overview for those developing applications in physically based modeling, VR, haptics, and robotics.

20 A personal view of the personal work station: some firsts in the Fifties

Douglas Ross
January 1986 **Proceedings of the ACM Conference on The history of personal workstations**
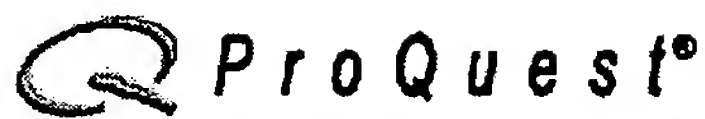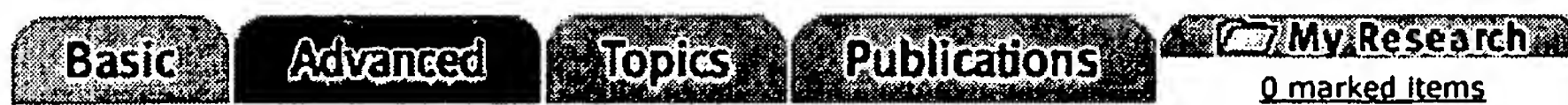**Publisher:** ACM Press
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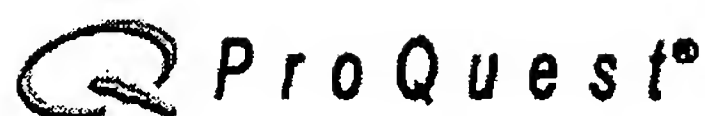
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1. **Component behavior simulation refined**
   Joe Greco. **DPN : Design Product News.** Willowdale: Jan/Feb 2002. Vol. 30, Iss. 1; p. 25 (1 page)
   📖 Abstract

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2. **Deals**
   Anonymous. **Manufacturing Engineering.** Dearborn: May 2001. Vol. 126, Iss. 5; p. 40 (2 pages)
   📄 Full text   📄 Page Image - PDF   📖 Citation

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3. **The advancing state of solid modeling**
   Robert Mills. **Computer - Aided Engineering.** Cleveland: Sep 1998. Vol. 17, Iss. 9; p. 56 (6 pages)
   📄 Text+Graphics   📄 Page Image - PDF   📖 Abstract

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4. **SolidWorks 98**
   Beverly A Beckert. **Computer - Aided Engineering.** Cleveland: May 1998. Vol. 17, Iss. 5; p. 28 (1 page)
   📄 Full text   📄 Page Image - PDF   📖 Abstract

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5. **Autofact '97: Making manufacturing accessible**
   Janmarie Knoth. **Computer - Aided Engineering.** Cleveland: Oct 1997. Vol. 16, Iss. 10; p. 36 (9 pages)
   📄 Text+Graphics   📄 Page Image - PDF   📖 Abstract

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6. **Cool CAE sites**
   Anonymous. **Computer - Aided Engineering.** Cleveland: Aug 1996. Vol. 15, Iss. 8; p. 88 (1 page)
   📄 Text+Graphics   📄 Page Image - PDF   📖 Citation

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7. **The constraint management company**
   Anonymous. **Computer - Aided Engineering.** Cleveland: Aug 1996. Vol. 15, Iss. 8; p. 26 (1 page)
   📄 Full text   📄 Page Image - PDF   📖 Abstract

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8. **ACIS Tech-Ex '96 explores 3D components**
   Woodlock, Janmarie. **Computer - Aided Engineering.** Cleveland: Mar 1996. Vol. 15, Iss. 3; p. 26 (1 page)
   📄 Full text   📄 Page Image - PDF   📖 Abstract

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9. **Autodesk Licenses Constraint Management Technology**
   Zarrillo, Andrew. **Business Wire.** New York: Feb 26, 1990. p. 1
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Canada NewsWire. Ottawa: Jun 30, 2004. p. 1

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PR Newswire. New York: Jun 30, 2004. p. 1

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☐ 3. **PLM supplier UGS to buy D-Cubed;**
Lisa Troshinsky. **Aerospace Daily & Defense Report.** Washington: Jun 9, 2004. Vol. 210, Iss. 49; p. 6

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Canada NewsWire. Ottawa: Jun 8, 2004. p. 1

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PR Newswire. New York: Jun 8, 2004. p. 1

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☐ 6. **Alibre Pursues Producers of RaceCAD for Stealing Alibre Design Source Code; Source Code Theft by Former Employee Casts Doubt on Outsourced Software Development in Russia and Other Countries**
Business Editors/High-Tech Writers. **Business Wire.** New York: Oct 23, 2003. p. 1

📄 Full text   🔲 Abstract

☐ 7. **Component behavior simulation refined**
Joe Greco. **DPN : Design Product News.** Willowdale: Jan/Feb 2002. Vol. 30, Iss. 1; p. 25 (1 page)

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☐ 8. **Autodesk Licenses Constraint Management Technology**
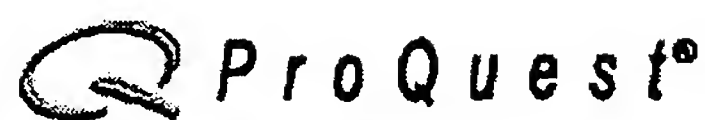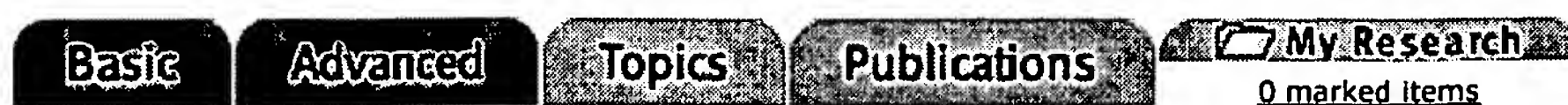*Zarrillo, Andrew.* **Business Wire.** New York: Feb 26, 1990. p. 1

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1. **A road map to solid modeling**
   Hoffmann, C.M.; Rossignac, J.R.
   Visualization and Computer Graphics, IEEE Transactions on
   Volume: 2 Issue: 1 Mar 1996
   Page(s): 3-10
   Digital Object Identifier 10.1109/2945.489381
   Summary: The objective of solid modeling is to represent, manipulate and reason about the 3D shape of sol
   computer. Such representations should be unambiguous. Solid modeling's major application areas include d
   compu.....

   AbstractPlus | References | Full Text: PDF   IEEE JNL

2. **Variational constraints in 3D**
   Durand, C.; Hoffmann, C.M.
   Shape Modeling and Applications, 1999. Proceedings. Shape Modeling International '99. International Confe
   1-4 Mar 1999
   Page(s): 90-97
   Digital Object Identifier 10.1109/SMA.1999.749328
   Summary: Geometric constraint solving is an integral part of computer aided design, serving the role both of
   geometric elements in relation to each other, as well as dimensioning and constraining their shapes accurate
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1. **Robust set operations on polyhedral solids**
   Hoffmann, C.M.; Hopcroft, J.E.; Karasick, M.J.;
   Computer Graphics and Applications. IEEE
   Volume 9, Issue 6, Nov. 1989 Page(s):50 - 59
   Digital Object Identifier 10.1109/38.41469

   AbstractPlus | Full Text: PDF(832 KB)   IEEE JNL
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2. **The problems of accuracy and robustness in geometric computation**
   Hoffmann, C.M.;
   Computer
   Volume 22, Issue 3, March 1989 Page(s):31 - 39, 41
   Digital Object Identifier 10.1109/2.16223

   AbstractPlus | Full Text: PDF(996 KB)   IEEE JNL
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3. **Implicit curves and surfaces in CAGD**
   Hoffmann, C.M.;
   Computer Graphics and Applications. IEEE
   Volume 13, Issue 1, Jan. 1993 Page(s):79 - 88
   Digital Object Identifier 10.1109/38.180121

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4. **A road map to solid modeling**
   Hoffmann, C.M.; Rossignac, J.R.;
   Visualization and Computer Graphics. IEEE Transactions on
   Volume 2, Issue 1, March 1996 Page(s):3 - 10
   Digital Object Identifier 10.1109/2945.489381

   AbstractPlus | References | Full Text: PDF(960 KB)   IEEE JNL
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5. **Variational constraints in 3D**
   Durand, C.; Hoffmann, C.M.;
   Shape Modeling and Applications. 1999. Proceedings. Shape Modeling International '99. Internatio
   1-4 March 1999 Page(s):90 - 97
   Digital Object Identifier 10.1109/SMA.1999.749328

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